

PRODUCT SPECIFICATION

1.77” TN TFT LCD MODULE
MODEL: YDP LCD TN 6 177 M 003



- <◇ > Preliminary Specification
- <◆ > Finally Specification

CUSTOMER’S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2015.10.06	ZFY	Initial Release	
1.1	2016.10.21	ZFY	Modify Current	P5
1.2	2018.04.02	ZDT	Modify Backlight Characteristic Add LED working life Modify many details	P5 P5 P21-P22

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver Ics and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	1.77"	
LCD type	TN TFT	
Display Mode	Transmissive /Normally white	
Resolution	128 RGB x 160	Pixels
View Direction	6 O'clock	Best Image
Gray Scale Inversion Direction	12 O'clock	
Module Outline	34.701(H) x 46.75 (V) x 2.25 (T) (Note1)	mm
Active Area	28.032 (H) x 35.04 (V)	mm
Pixel Size	219 (H) x 219 (V)	um
Pixel Arrangement	Stripe	
Polarizer Surface Treatment	Anti-glare	
Display Colors	262K	
Interface	MCU 8bit	
With or without touch panel	Without	
Driver IC	ILI9163V	-
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	5	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25°C

Item		Symbol	Min.	Max.	Unit
Supply Voltage	Logic	IOVCC	-0.3	4.6	V
	Analog	VCC	-0.3	4.6	V
Storage temperature		T _{STG}	-30	+80	°C
Operating temperature		T _{OP}	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	Logic	IOVCC	1.65	1.8	3.3	V
	Analog	VCC	2.5	2.8	3.3	V
Logic Low input voltage		V_{IL}	GND	-	$0.3 \cdot IOVCC$	V
Logic High input voltage		V_{IH}	$0.7 \cdot IOVCC$	-	IOVCC	V
Logic Low output voltage		V_{OL}	GND	-	$0.2 \cdot IOVCC$	V
Logic High output voltage		V_{OH}	$0.8 \cdot IOVCC$	-	IOVCC	V
Current Consumption All Black	Logic	$I_{CC} + I_{IN}$	-	2	-	mA
	Analog					

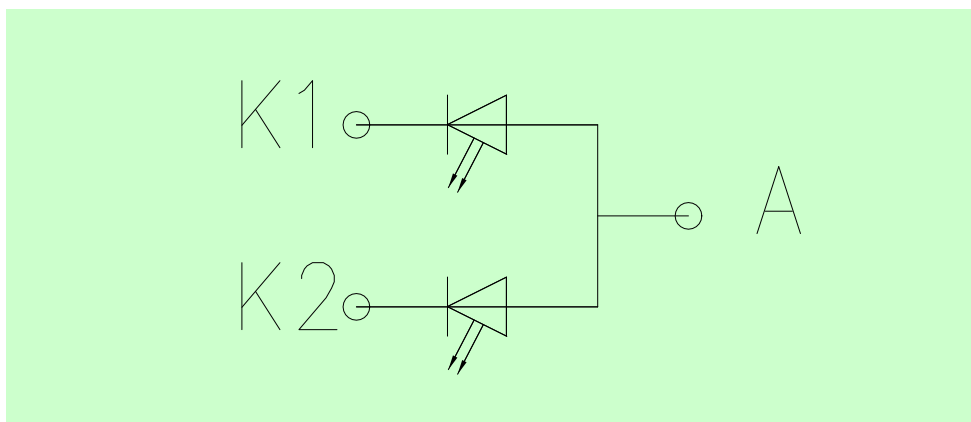
5. Backlight Characteristic

5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_F	$T_a = 25^\circ\text{C}$, $I_F = 15\text{mA/LED}$	2.8	3.2	3.4	V
Forward Current	I_F	$T_a = 25^\circ\text{C}$, $V_F = 3.2\text{V/LED}$	-	30	-	mA
Power dissipation	P_D		-	96	-	mW
Uniformity	Avg		-	80	-	%
LED working life(25°C)	-		-	30,000	-	Hrs
Drive method	Constant current					
LED Configuration	2 White LEDs in parallel					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.
The environmental conducted under ambient air flow, at $T_a = 25 \pm 2^\circ\text{C}$, $60\%RH \pm 5\%$, $I_F = 20\text{mA}$

5.2. Backlight Characteristic



6. Optical Characteristics

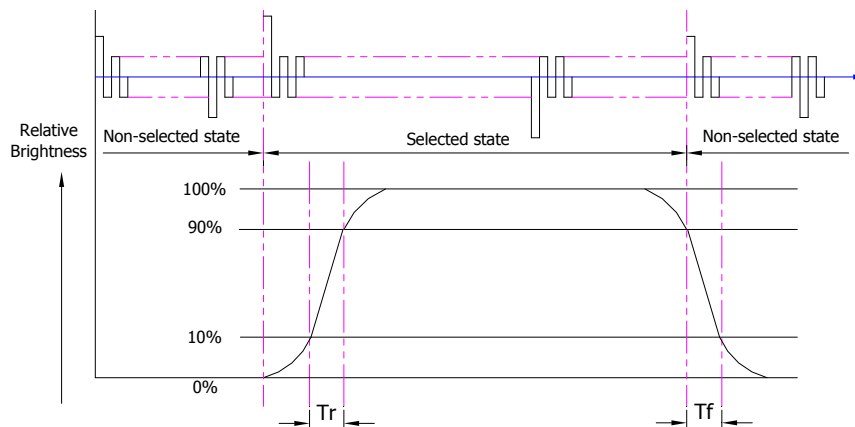
6.1. Optical Characteristics

Ta=25°C, VCC=2.8V

Backlight On (Transmissive Mode)	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
	Luminance on TFT(I_f =15mA/LED)		Lv	Normally viewing angle $\theta_x = \varphi_y = 0^\circ$	290	350	-	cd/m ²
	Contrast ratio(See 6.3)		CR		200	300	-	
	Response time (See 6.2)		T _R +T _F		-	30	60	ms
	Chromaticity Transmissive (See 6.5)	Red	X _R		0.527	0.577	0.627	
			Y _R		0.295	0.345	0.395	
		Green	X _G		0.256	0.316	0.366	
			Y _G		0.577	0.627	0.677	
		Blue	X _B		0.095	0.145	0.195	
			Y _B		0.027	0.077	0.127	
		White	X _W		0.230	0.280	0.330	
			Y _W		0.272	0.322	0.372	
	Viewing Angle (See 6.4)	Horizontal	θ _x +	Center CR≥10	40	45	-	Deg.
			θ _x -		40	45	-	
		Vertical	φ _y +		40	45	-	
φ _y -			15		20	-		
NTSC Ratio(Gamut)				-	(60)	-	%	

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

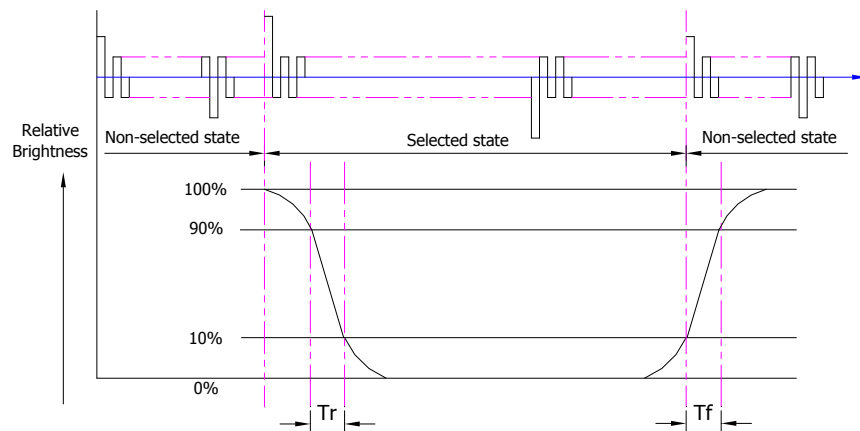


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected state with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

6.3. Definition of Contrast Ratio

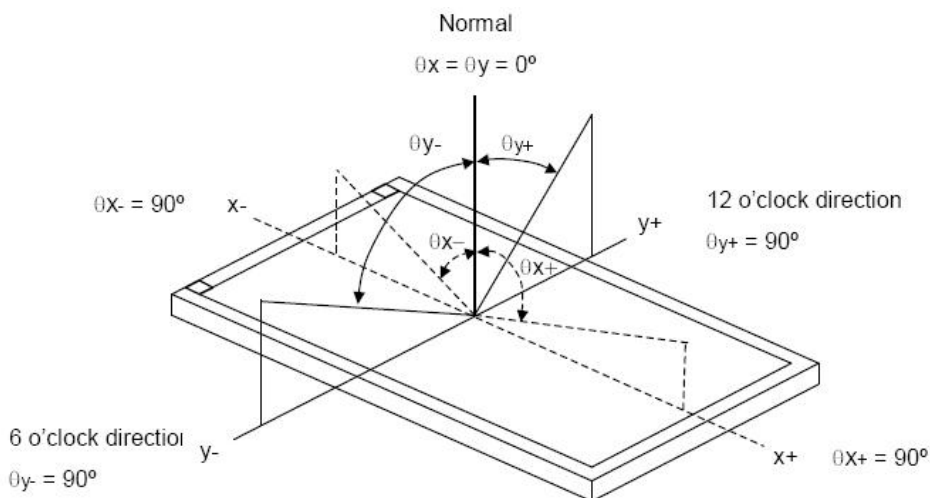
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



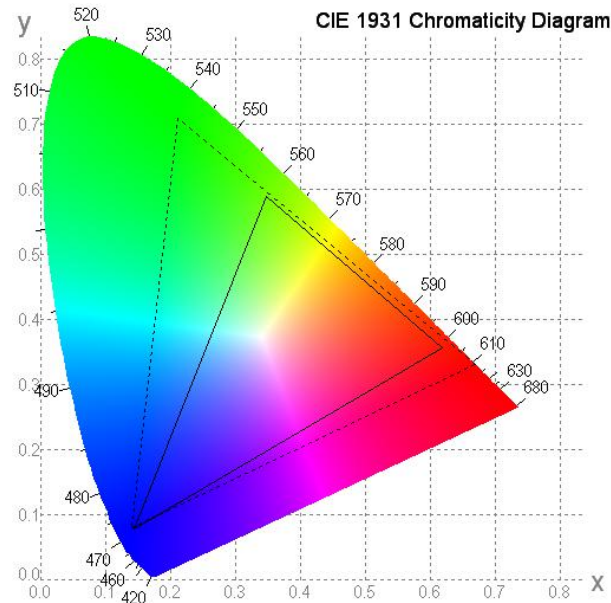
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.6. Definition of Surface Luminance, Uniformity and Transmittance

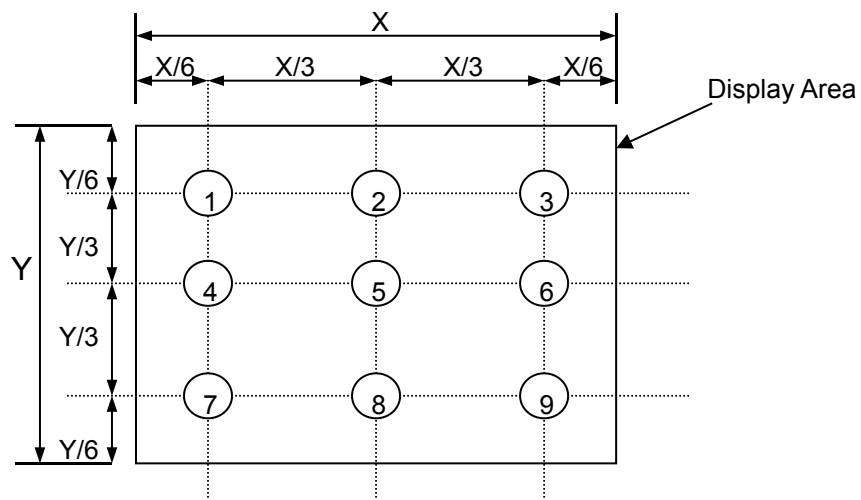
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1. Surface Luminance: $L_V = \text{average } (L_{P1}:L_{P9})$

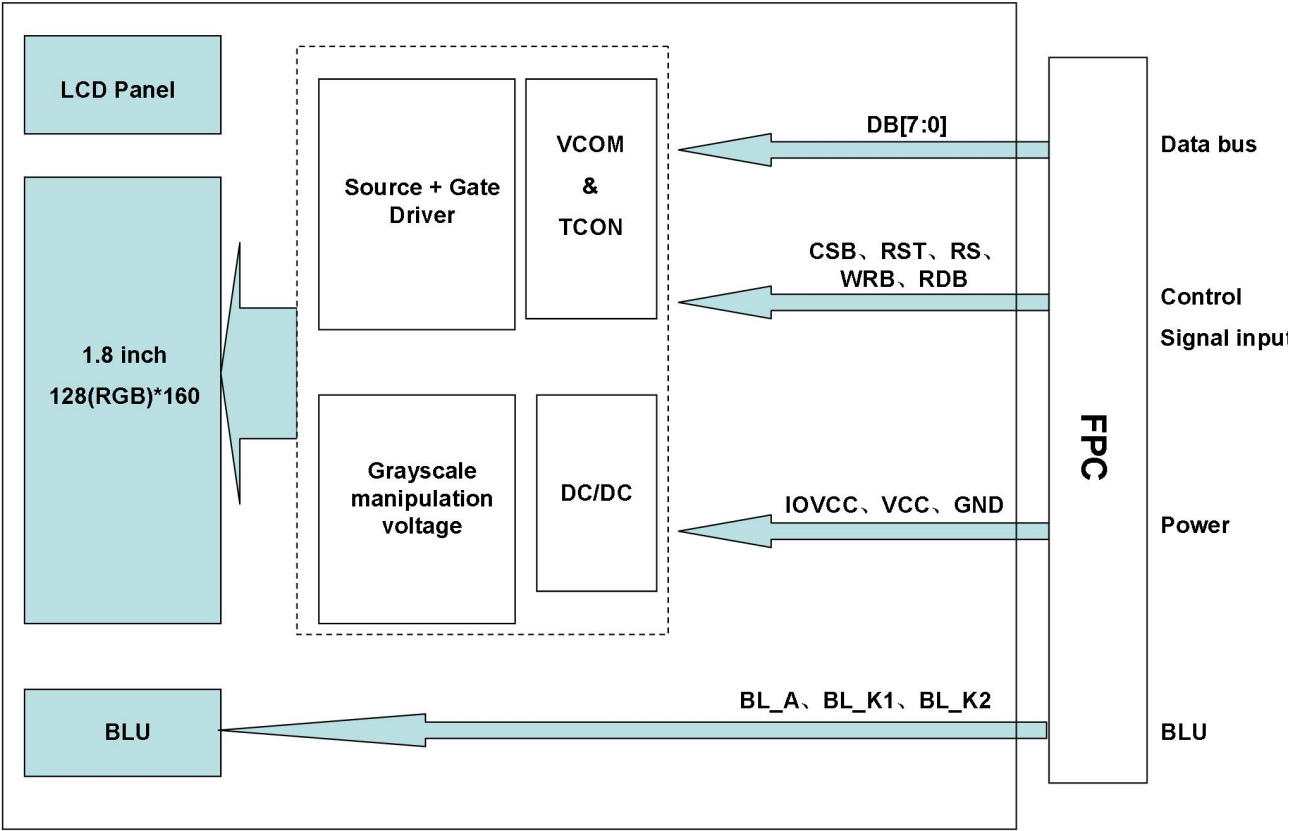
6.6.2. Uniformity = Minimal ($L_{P1}:L_{P9}$) / Maximal ($L_{P1}:L_{P9}$) * 100%

6.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply

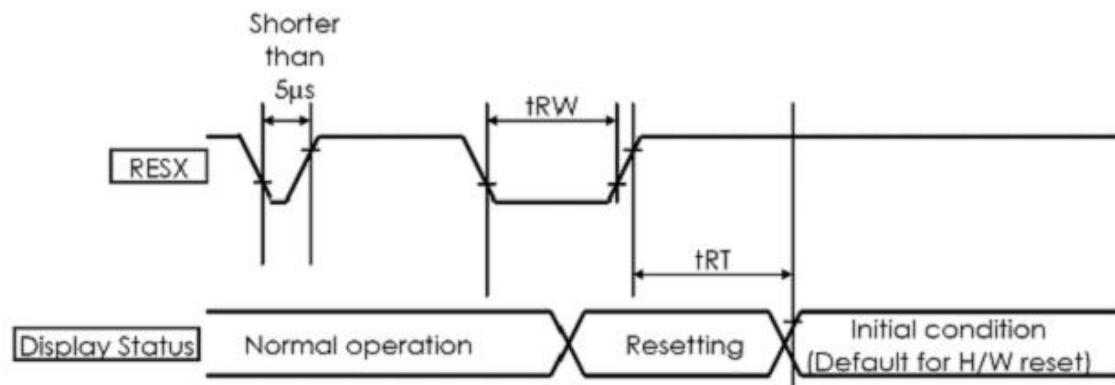


8. Interface Pins Definition

No.	Symbol	Function	Remark
1	GND	Ground	
2	IOVCC	Logic Power	
3	VCC	Analog Power	
4	CSB	Chip Select Signal	
5	RST	Reset Signal	
6	RS	Data/Command Signal	
7	WRB	Write signal	
8	RDB	Read signal	
9	DB0	Data bus	
10	DB1	Data bus	
11	DB2	Data bus	
12	DB3	Data bus	
13	DB4	Data bus	
14	DB5	Data bus	
15	DB6	Data bus	
16	DB7	Data bus	
17	BL_A	Led anode	
18	BL_K1	Led cathode	
19	BL_K2	Led cathode	
20	GND	Ground	

9. AC Characteristics

9.1. Reset timing



(VSS=0V, VDDI=1.65V to 1.95V, VPNL=2.6V to 2.9V, Ta = -30 to 70°C)

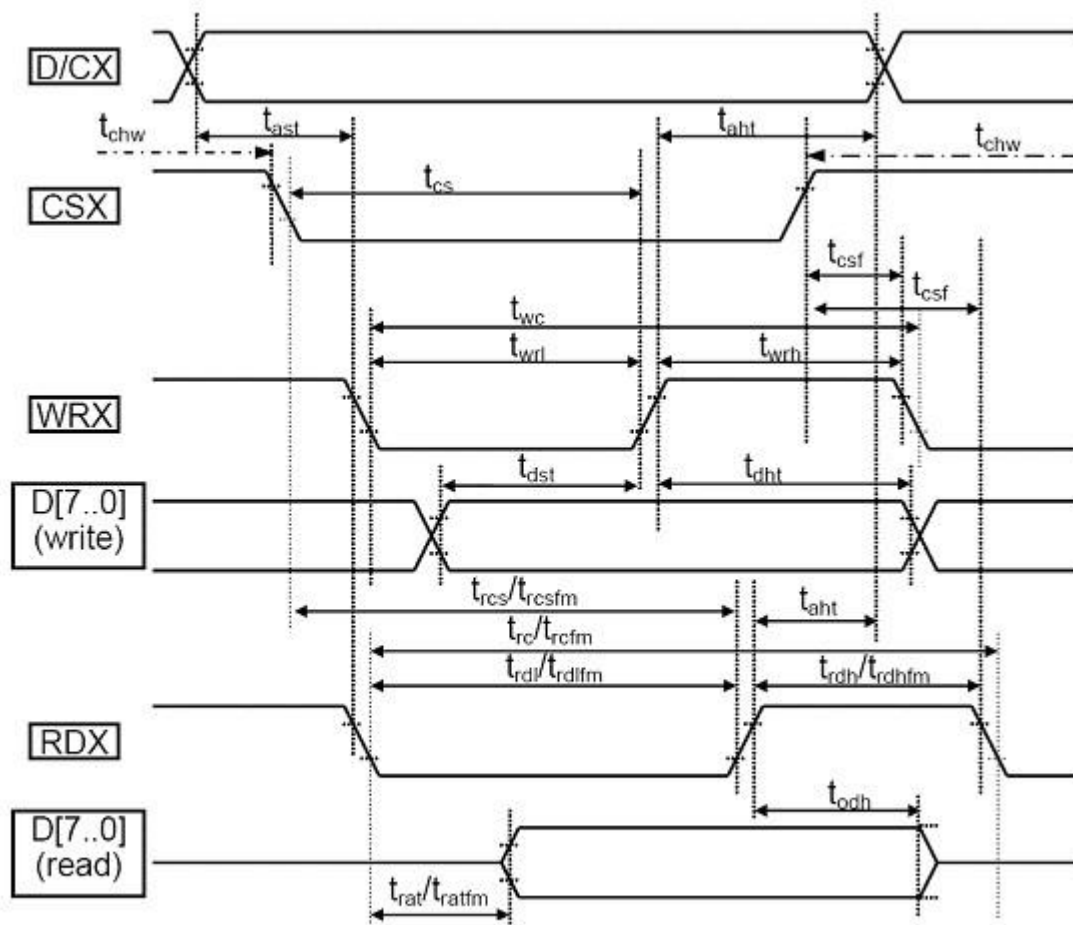
Symbol	Parameter	Related Pins	MIN	TYP	MAX	Note	Unit
tRESW	*1) Reset low pulse width	RESX	10	-	-	-	µs
tREST	*2) Reset complete width	-	-	-	5	When reset applied during Sleep in mode	ms
		-	-	-	120	When reset applied during Sleep out mode	ms

Note

1. Spike due to an electrostatic discharge on RESX line does not cause system reset according to the table below.

9.2. Interface timing

Display Module Parallel 18/16/9/8-bit Bus



Note: Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.

Signal	Symbol	Parameter	min	max	unit	description
D/CX	tast	Address setup time	0		ns	
	taht	Address hold time(Write/Read)	10		ns	
CSX	tchwh	"S""H" Pulse Width	0		ns	
	tcs	Chip Select setup time (Write)	10		ns	
	trcs	Chip Select setup time (Read ID)	45		ns	
	trcsfm	Chip Select setup time (Read FM)	355		ns	
	tcsf	Chip Select Wait time(Write/read)	10		ns	

WRX	twc	Write cycle	66		ns	
	twrh	Control pulse H duration	15		ns	
	twrl	Control pulse L duration	15		ns	
RDX	trc	Read cycle (ID)	160		ns	When read ID data
	trdh	Control pulse H duration(ID)	90		ns	
	trdl	Control pulse L duration(ID)	45		ns	
RDX	trcfm	Read cycle (FM)	450		ns	When read from frame memory
	trdhfm	Control pulse H duration (FM)	90		ns	
	trdlfm	Control pulse L duration (FM)	355		ns	
D[17..0]	tdst	Data setup time	10		ns	For maximum CL = 30pF For minimum CL = 8pF
	tdht	Data hold time	10		ns	
	trat	Read access time (ID)		40	ns	
	trafm	Read access time (FM)		340	ns	
	todh	Output disable time	20	80	ns	

Note 1: VDDI 1.65 to 3.3V, VDD=2.6 to 3.3V, AGND=GND=0V, Ta=-30 to 70 °C (to +85°C no damage)

Note 2: This input signal rise time and fall time (tr, tf) is specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for input signals

10. Quality Assurance

10.1. Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

10.2. Standard for Quality Test

10.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

10.2.2. Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

10.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.3. Nonconforming Analysis & Disposition

10.3.1. Nonconforming analysis:

10.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

10.3.2. Disposition of nonconforming:

10.3.2.1. Non-conforming product over PPM level will be replaced.

10.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

10.4. Agreement Items

Shall negotiate with customer if the following situation occurs:

10.4.1. There is any discrepancy in standard of quality assurance.

10.4.2. Additional requirement to be added in product specification.

10.4.3. Any other special problem.

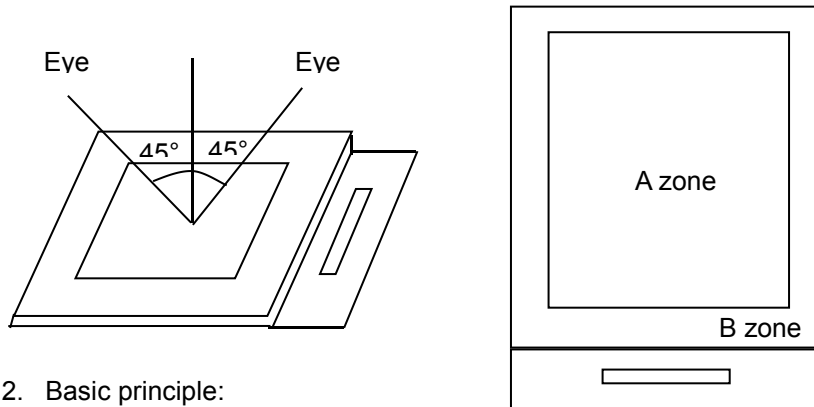
10.5. Standard of the Product Visual Inspection

10.5.1. Appearance inspection:

10.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

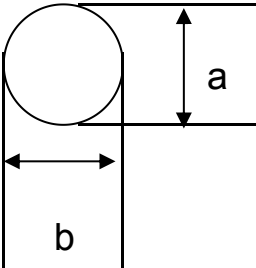


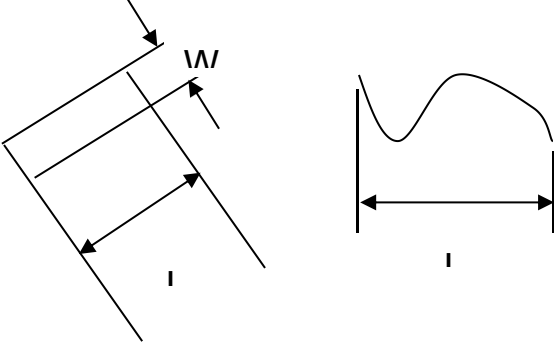
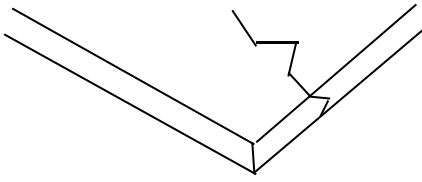
10.5.2. Basic principle:

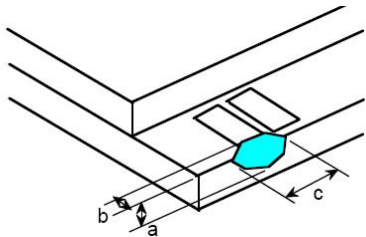
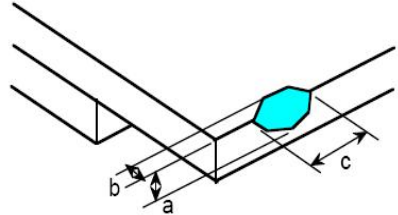
10.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

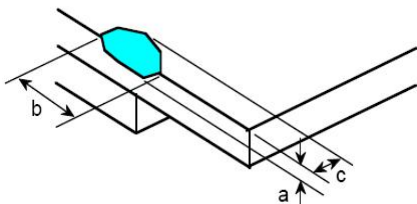
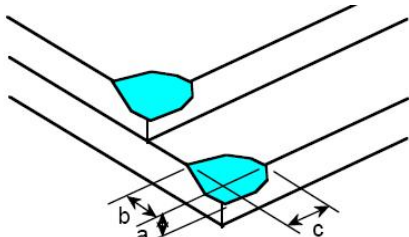
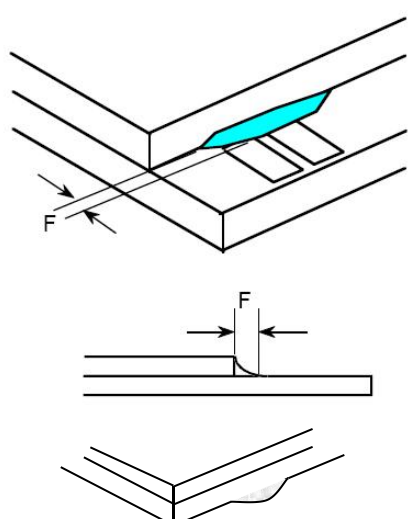
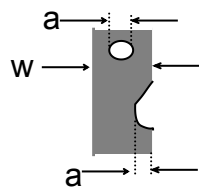
10.5.2.2. New item must be added on time when it is necessary.

10.6. Inspection Specification

No.	Item	Criteria (Unit: mm)																		
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 $\varphi = (a + b) / 2$ Distance between 2 defects should more than 3mm apart.	<table><tr><th>Size \ Area</th><th>Acc. Qty</th></tr><tr><td>$\varphi \leq 0.10$</td><td>Ignore</td></tr><tr><td>$0.10 < \varphi \leq 0.15$</td><td>2</td></tr><tr><td>$0.15 < \varphi \leq 0.25$</td><td>1</td></tr><tr><td>$0.25 < \varphi$</td><td>0</td></tr><tr><td>Total</td><td>2 no include $\varphi \leq 0.10$</td></tr></table>	Size \ Area	Acc. Qty	$\varphi \leq 0.10$	Ignore	$0.10 < \varphi \leq 0.15$	2	$0.15 < \varphi \leq 0.25$	1	$0.25 < \varphi$	0	Total	2 no include $\varphi \leq 0.10$					
			Size \ Area	Acc. Qty																
$\varphi \leq 0.10$	Ignore																			
$0.10 < \varphi \leq 0.15$	2																			
$0.15 < \varphi \leq 0.25$	1																			
$0.25 < \varphi$	0																			
Total	2 no include $\varphi \leq 0.10$																			
02	Electrical Defect (Minor defect)	<table><tr><td></td><td>Display Area</td><td>Total</td><td rowspan="3">Note1</td></tr><tr><td>Bright dot</td><td>0</td><td>0</td></tr><tr><td>Dark dot</td><td>$N \leq 2$</td><td>$N \leq 2$</td></tr><tr><td>Total dot</td><td>$N \leq 2$</td><td>$N \leq 2$</td><td rowspan="2">Note2</td></tr><tr><td>Mura</td><td colspan="2">Not visible through 5% ND filters.</td></tr></table>		Display Area	Total	Note1	Bright dot	0	0	Dark dot	$N \leq 2$	$N \leq 2$	Total dot	$N \leq 2$	$N \leq 2$	Note2	Mura	Not visible through 5% ND filters.		
				Display Area	Total		Note1													
Bright dot	0	0																		
Dark dot	$N \leq 2$	$N \leq 2$																		
Total dot	$N \leq 2$	$N \leq 2$	Note2																	
Mura	Not visible through 5% ND filters.																			
		Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.																		

03	Black and White line Scratch Foreign material (Line type) (Minor defect)	 <table border="1" data-bbox="614 582 1236 891"> <thead> <tr> <th>Length</th><th>Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>/</td><td>$W \leq 0.03$</td><td>Ignore</td></tr> <tr> <td>$L \leq 2.5$</td><td>$0.03 < W \leq 0.05$</td><td>3</td></tr> <tr> <td>$L \leq 2.5$</td><td>$0.05 < W \leq 0.10$</td><td>2</td></tr> <tr> <td>/</td><td>$0.1 < W$</td><td>0</td></tr> <tr> <td colspan="2">Total</td><td>3</td></tr> </tbody> </table> Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.	Length	Width	Acc. Qty	/	$W \leq 0.03$	Ignore	$L \leq 2.5$	$0.03 < W \leq 0.05$	3	$L \leq 2.5$	$0.05 < W \leq 0.10$	2	/	$0.1 < W$	0	Total		3
Length	Width	Acc. Qty																		
/	$W \leq 0.03$	Ignore																		
$L \leq 2.5$	$0.03 < W \leq 0.05$	3																		
$L \leq 2.5$	$0.05 < W \leq 0.10$	2																		
/	$0.1 < W$	0																		
Total		3																		
04	Glass Crack (Minor defect)	 Crack is potential to enlarge, any type is not allowed.																		

05	Glass Chipping Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>3</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$			
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	3											
$a < \text{Glass Thickness}$												
06	Glass Chipping Rear of Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												

07	Glass Chipping Except Pad Area: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c > 3.0, b < 1.0$</td><td>1</td></tr><tr><td>$c < 3.0, b < 1.0$</td><td>2</td></tr><tr><td>$c < 3.0, b < 0.5$</td><td>4</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
08	Glass Corner Chipping: (Minor defect) 	<table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td>$c < 3.0, b < 3.0$</td><td>Ignore</td></tr><tr><td colspan="2">$a < \text{Glass Thickness}$</td></tr></table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	Glass Burr: (Minor defect) 	<table><tr><th>Length</th><th>Acc. Qty</th></tr><tr><td>$F < 1.0$</td><td>Ignore</td></tr></table> Glass burr don't affect assemble and module dimension.	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											
10	FPC Defect: (Minor defect) 	10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.										

11	Bubble on Polarizer (Minor defect)		Diameter	Acc. Qty
			$\varphi \leq 0.20$	Ignore
			$0.20 < \varphi \leq 0.30$	4
			$0.30 < \varphi \leq 0.50$	1
			$0.50 < \varphi$	None
12	Dent on Polarizer (Minor defect)		Diameter	Acc. Qty
			$\varphi \leq 0.20$	Ignore
			$0.20 < \varphi \leq 0.30$	4
			$0.30 < \varphi \leq 0.50$	1
			$0.50 < \varphi$	None
13	Bezel	13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.		
14	Touch Panel	D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.		
15	PCB	15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.		
16	Soldering	Follow IPC-A-610C standard		
17	Electrical Defect (Major defect)	The below defects must be rejected. 17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.		

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

10.7. Classification of Defects

10.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

10.7.2. Two minor defects are equal to one major in lot sampling inspection.

10.8. Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

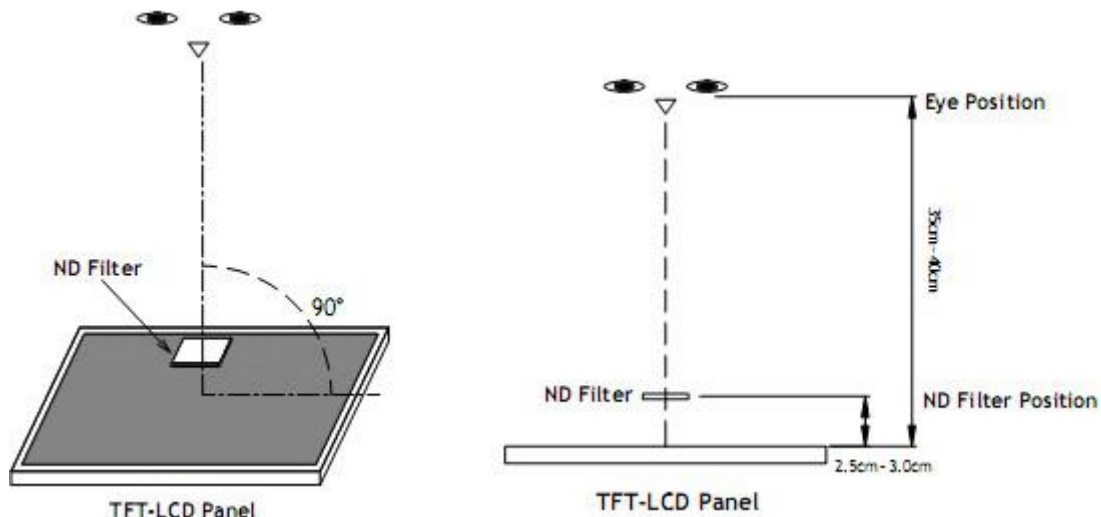
10.9. Packaging

10.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

10.9.2. Modules inside package box should have compliant mark.

10.9.3. All direct package materials shall offer ESD protection

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

11. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	50°C, 90%RH, 96Hrs	2	GB/T2423.3-2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: $\pm 8KV$ 150pF/330 Ω 5 times Contact: $\pm 4KV$ 150pF/330 Ω 5 times	2	GB/T17626.2-2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

12. Precautions and Warranty

12.1. Safety

- 12.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 12.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2. Handling

- 12.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 12.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3. Storage

- 12.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 12.3.2. Strong light exposure causes degradation of polarizer and color filter.

12.4. Metal Pin (Apply to Products with Metal Pins)

12.4.1. Pins of LCD and Backlight

- 12.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

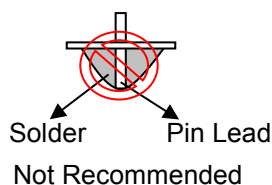
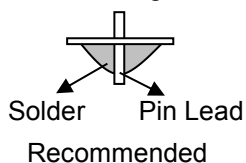
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

12.4.1.3. Solder Wetting



12.4.2. Pins of EL

- 12.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

- 12.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290℃

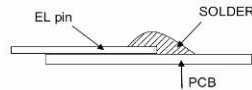
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body): 2.0mm

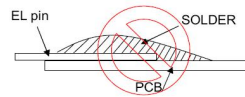
- 12.4.2.4. No horizontal press on the EL leads during soldering.

- 12.4.2.5. 180° bend EL leads three times is not allowed.

12.4.2.6. Solder Wetting

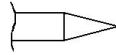


Recommended

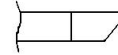


Not Recommended

12.4.2.7. The type of the solder iron:

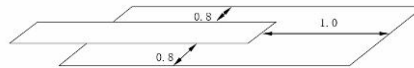


Recommended



Not Recommended

12.4.2.8. Solder Pad



12.5. Operation

- 12.5.1. Do not drive LCD with DC voltage
- 12.5.2. Response time will increase below lower temperature
- 12.5.3. Display may change color with different temperature
- 12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 12.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 12.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 12.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 12.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

12.6. Static Electricity

- 12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 12.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7. Limited Warranty

- 12.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 12.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 12.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Packaging

TBD

QP-001-027

